

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Appl. No. : 10/534,962 Confirmation No. 1696  
Applicant(s) : KONDO, Masayoshi et al.  
Filed : May 16, 2005  
TC/A.U. : 2841  
Examiner : Ishwarbhai B.Patel

Title : **CIRCUIT BOARDS, MULTILAYER WIRING BOARDS, METHOD  
OF PRODUCING CIRCUIT BOARDS, AND METHOD OF  
PRODUCING MULTILAYER WIRING BOARDS**

Docket No. : 033036.088  
Customer No. : 25461

**MAIL STOP AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450  
Sir:

**AMENDMENT**

In response to the Office Action of September 7, 2007, please amend the application as follows:

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this paper.

**Remarks** begin on page 10 of this paper.